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U.S. UTILITY Patent Application

APPL NUM 10020111	FILING DATE 12/14/2001	CLASS 257	SUBCLASS 301	GAU 2811	EXAMINER Booth
**APPLICANTS: Wasshuber Christoph; Joyner Keith 438 2812					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>		ATTORNEY DOCKET NO	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		TI-31015	
Verified and Acknowledged Examiners's initials					
TITLE : Methods and apparatus for inducing stress in a semiconductor device					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-435 (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	
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